IN THE UNITED STATES PATENT AND TRADEMARK OFFICE Applicant(s): TOMERLIN et al. Group Art Unit: Application No.: Filed: Examiner: Title: Attorney Docket No.: CML01079J **Commissioner for Patents** P.O. Box 1450 Alexandria, VA 22313-1450 INFORMATION DISCLOSURE STATEMENT Dear Sir: This Information Disclosure Statement is submitted: X under 37 CFR §1.97(a), or (Accompanies the new patent application submitted herewith.) under 37 CFR 1.97(b), or (Within three months of filing national application; or date of entry of international application; or before mailing date of first office action on the merits; whichever occurs last.) under 37 CFR 1.97(c) together with either a: ___ Statement under 37 CFR 1.97(e), or a \$180.00 fee under 37 CFR 1.17(p), or (After the CFR 1.97(b) time period, but before final action or notice of allowance, whichever occurs first.) under 37 CFR 1.97(d) together with a: Statement under 37 CFR 1.97(e), and a \$180.00 fee set forth in 37 CFR 1.17(p). (Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee.) Applicant(s) submit herewith Form PTO 1449-Information Disclosure Citation together with required copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56. It is requested that the information disclosed herein be made of record in this application... I hereby certify that this correspondence is being deposited Respectfully submitted, with the United States Postal Service as Express Mail ER 335372093 US in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. Renee Michelle Larson Date of Deposit: January 29, 2004 Attorney/Agent for Applicant(s)

Typed Name: Renee Michelle Larson

Date: January 29, 2004

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U.S. PATENT DOCUMENTS						
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		US-6,282,627	08-28-2001	Wong et al.		
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	SPATARO, ANNE ET AL. "A VLSI CMOS Delay Oriented Waveform Converter for Polyphase Frequency Synthesizer." IEEE Journal of Solid-State Circuits, March 2002, pp. 336-341, vol. 37.				
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